



OFFICE OF THE CONTROLLER OF EXAMINATIONS
MAHATMA GANDHI UNIVERSITY
(Accredited with 'B+' grade by NAAC)
YELLAREDDYGUDA, NALGONDA - 508 254
(www.mguniversity.ac.in)

Lr.No.1413/A/MGU/B.Tech/Exams/2025-26

Date:17.03.2026

TIME-TABLE

B. Tech. (CSE /ECE/EEE) V-Semester Backlog (R23) Examinations, April-2026

Time: 02.00 PM to 05.00PM

Date	Paper	CSE	ECE	EEE
01.04.2026 (Wednesday)	I	Machine Learning	Antenna and Wave Propagation	Linear Control Systems
04.04.2026 (Saturday)	II	Database Management Systems	Digital System Design using Verilog HDL	Switchgear and Protection
07.04.2026 (Tuesday)	III	Computer Networks	Digital Communication	Electrical Measurements & Instrumentation
09.04.2026 (Thursday)	IV	Software Engineering	Linear Control Systems	Linear Integrated Circuits
16.04.2026 (Thursday)	V	Distributed Systems	Micro Controllers and Interfacing	Python Programming & Applications
18.04.2026 (Saturday)	<u>VI</u>	Professional Elective-II Image Processing / Data Mining / Computer Graphics / Digital Forensics	Computer Architecture and Organization	Professional Elective-II Electrical Distribution Systems/ renewable Energy Sources / Reliability Engineering
21.04.2026 (Tuesday)	<u>VII</u>	-----	Professional Elective-II Artificial Intelligence & Machine Learning / Digital Image Processing / Object Oriented Programming using C++	-----

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TIME-TABLE

B. Tech. (CSE /ECE/EEE) V-Semester Backlog (AICTE) Examinations, April-2026

Time: 02.00 PM to 05.00PM

Date	Paper	AICTE		
		CSE	ECE	EEE
01.04.2026 (Wednesday)	I	Principles of Management	Linear Control Systems	Electrical Machines – III
04.04.2026 (Saturday)	II	Fundamentals of Data Sciences	Analog Communication	Measurements and Instrumentation
07.04.2026 (Tuesday)	III	Database Management Systems	Digital Communication	Linear Control Systems
09.04.2026 (Thursday)	IV	Software Engineering	Microprocessor & Micro Controller	Microprocessors & Micro Controllers
16.04.2026 (Thursday)	V	Automata Languages & Computation	<u>Professional Elective– I</u> 1.Digital image Processing 2.Electronic Measurements and Instrumentation 3.Object Oriented Programming Language Systems 4.Digital System Design using Verilog HDL	Power Electronics
18.04.2026 (Saturday)	VI	<u>Professional Elective-I</u> 1.Principles of Programming Languages 2.Advanced Operating Systems 3.Graph Theory	<u>Mandatory Course</u> 1.Technical Communication and soft skills 2.Indian Constitution 3.Essence of Indian Traditional Knowledge	<u>Open Elective – I</u> 1.Material Handling 2.Disaster Management 3.Geospatial Techniques 4.Operating Systems 5.Reliability Engineering 6.Embedded Systems 7.Digital System Design Using Verilog HDL

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Date:17.03.2026

TIME-TABLE

B. Tech. (CSE /ECE/EEE) V-Semester (CBCS) Backlog Examinations, April, 2026

Timings: 02.00pm to 05.00 pm

Date	Paper	CBCS		
		CSE	ECE	EEE
01.04.2026 (Wednesday)	I	Database Management Systems	Linear IC's and applications	Power Systems-II
04.04.2026 (Saturday)	II	Design Analysis of Algorithms	Microprocessors and Microcontrollers	Electrical Machines-II
07.04.2026 (Tuesday)	III	Compiler Design	Analog Communications	Electrical Measurements & Instrumentation
09.04.2026 (Thursday)	IV	Software Engineering	Automatic Control Systems	Linear Control Systems
16.04.2026 (Thursday)	V	Managerial Economics & Accountancy	Electronic Instrumentation	Professional Elective-I 1. Electrical Distribution systems 2. Renewable Energy Sources 3. High Voltage Engineering
18.04.2026 (Saturday)	VI	Professional Elective – I 1. Advanced Computer Architecture 2. Advance data Structures 3. Computer Graphics 4. Scripting Languages	Open Elective-I 1. OOPs using JAVA 2. Reliability Engineering 3. Computer Graphics 4. Disaster Management 5. Digital System Design using Verilog HDL	Professional Elective-II 1. Power System Reliability 2. Fuzzy Systems & Genetic \ Algorithms 3. Electrical Estimation Costing & Safety


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Note:

1. The candidates whose forms are rejected by the Examination Branch should not be examined and if any candidate is found not eligible at the later stage his/her registration will be cancelled for this exam. Any omission or clash may be intimated to the Controller of Examinations, MGU, immediately on the receipt of the Time Table.
2. Cell Phones and programmable calculators are strictly not allowed into the Examination Hall.
3. The responsibility of obtaining correct question paper from the Invigilator at the Examination Hall rests with the candidate. Answering a wrong Question paper may lead to Cancellation of Result.
4. The candidates have to carry the college identity card along with their Hall Ticket during the Examination.



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TIME-TABLE

B. Tech. (CSE /ECE/EEE) III-I-Semester (Non-CBCS) Backlog Examinations, April, 2026

Timings: 02.00 pm to 05.00 pm

Date	Paper	NON CBCS		
		CSE	ECE	EEE
01.04.2026 (Wednesday)	I	Database management systems	Liner Integrated Circuits and Applications	Power Systems-II
04.04.2026 (Saturday)	II	Operating systems	Digital Integrated Circuits and Applications	Electrical Machines-II
07.04.2026 (Tuesday)	III	Automata Languages and Computation	Analog Communications	Power Electronics
09.04.2026 (Thursday)	IV	Software Engineering	Automatic Control Systems	Digital Electronics and Logic Design
16.04.2026 (Thursday)	V	Managerial Economics and Accountancy	Computer Organization and Architecture	Liner Integrated Circuits
18.04.2026 (Saturday)	VI	Design and Analysis and Algorithms	Electronic Instrumentation	Linear Control Systems


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