



OFFICE OF THE CONTROLLER OF EXAMINATIONS
MAHATMA GANDHI UNIVERSITY
(Accredited with 'B+' grade by NAAC)
YELLAREDDYGUDA, NALGONDA-508254.(T.G.)
(www.mguniversity.ac.in)

Lr.No: 77/A/MGU /B.Tech /Exams/2026-27

Date : 15.05.2026

TIME-TABLE

B. Tech. (CSE /ECE/EEE) Semester-I Backlog (AICTE) Examinations, May-2026

Timings: 10.00 am to 01.00 pm

Date	Paper	CSE	EEE	ECE
05.05.2026 (Tuesday)	I	Engineering Physics	Engineering Physics	Engineering Chemistry
07.05.2026 (Thursday)	II	Mathematics-I	Mathematics-I	Mathematics-I
11.05.2026 (Monday)	III	Basic Electrical Engineering	English	Basic Electrical Engineering
13.05.2026 (Wednesday)	IV	Engineering Graphics	Engineering Graphics	Engineering Graphics

Controller of Examinations

Copy to:

1. The Principal of UCE & T MGU.
2. The Director, Academic Affairs/Dean, CDC, MGU.
3. The Secretary to Vice-Chancellor, MGU.
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TIME-TABLE

B. Tech. (CSE /ECE/EEE) Semester-II Backlog (AICTE) Examinations, May-2026

Timings: 2.00 pm to 5.00pm

Date	Paper	CSE	EEE	ECE
06.05.2026 (Wednesday)	I	Engineering Chemistry	Engineering Chemistry	Engineering Physics
08.05.2026 (Friday)	II	Mathematics - II	Mathematics- II	Mathematics - II
12.05.2026 (Tuesday)	III	Programming for Problem Solving	Programming for Problem Solving	Programming for Problem Solving
14.05.2026 (Thursday)	IV	English	Basic Electrical Engineering	English
16.05.2026 (Saturday)				

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TIME-TABLE

B.Tech. (CSE /ECE/EEE) III-Semester Backlog (AICTE) Examinations, May-2026

Timings: 10.00 am to 01.00

pm

Date	Paper	CSE	ECE	EEE
06.05.2026 (Wednesday)	I	Mathematics – III (Probability and Statistics)	Mathematics – III (Partial Differential Equations and Numerical Methods)	Mathematics – III
08.05.2026 (Friday)	II	Digital Logic Design	Managerial Economics and Accountancy	Electrical Circuit – I
12.05.2026 (Tuesday)	III	Basic Electronic Engineering	Electronic Devices and Circuits	Electrical Machines – I
14.05.2026 (Thursday)	IV	Data Structures Using C++	Digital System Design	Power Systems – I
16.05.2026 (Saturday)	V	Computer Organization & Architecture	Signal Analysis and Transform Techniques	Electromagnetic Fields
19.05.2026 (Tuesday)	VI	--	Network Analysis and Synthesis	Analog Electronics
21.05.2026 (Thursday)	VII	--	--	Environmental Science


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TIME-TABLE

B. Tech.(CSE /ECE/EEE) - IV-Semester (AICTE) Backlog Examinations May-2026

Timings: 2.00 pm to 5.00pm

Date	Paper	CSE	ECE	EEE
05.05.2026 (Tuesday)	I	Operations Research	Analog Electronic Circuits	Electrical Circuits- II
07.05.2026 (Thursday)	II	Business Economics & Financial analysis	Probability Theory & Stochastic Processes	Electrical Machines - II
11.05.2026 (Monday)	III	Object Oriented Programming through Java	Electromagnetic Waves and Transmission Lines	Power Systems- II
13.05.2026 (Wednesday)	IV	Design & Analysis of Algorithms	Pulse & Integrated Circuits	Digital Electronics and Logic Design
15.05.2026 (Friday)	V	Operating Systems	Mathematics- IV	Prime Movers & Pumps
18.05.2026 (Monday)	VI	Environmental Science	Elements of Mechanical Engineering	-----
20.05.2026 (Wednesday)	VII	-----	Environmental Science	-----


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