



OFFICE OF THE CONTROLLER OF EXAMINATIONS

MAHATMA GANDHI UNIVERSITY
NALGONDA - 508 254

No.154/Exams/MGU/2017-18

Date: 18.11.2017

TIME-TABLE

B.Tech. (CSE /ECE/EEE) II-Year I-Semester Backlog Examinations will be held in November/December, 2017 as per the following schedule.

Time: 2.00 pm to 5.00 pm

Date & Day	Paper	CSE	ECE	EEE
23.11.2017 Thursday	I	Mathematics-III	Mathematics-III	Mathematics-III
25.11.2017 Saturday	II	Data Structures Using C++	Basic Circuits Analysis	Electrical Circuits-I
28.11.2017 Tuesday	III	Discrete Structures	Electromagnetic Theory	Environmental Studies
30.11.2017 Thursday	IV	Logic and Switching Theory	Electronic Devices	Electrical Measurements and Instruments
04.12.2017 Monday	V	Computer Architecture	Elements of Mechanical Engineering	Electronic Engineering
06.12.2017 Wednesday	VI	Basic Electronics	Electrical Technology	Principles of Mechanical Engineering

A. Ravi

Controller of Examinations

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1. The Principal UCET, MGU
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5. PRO, MGU



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TIME-TABLE

B.Tech. (CSE /ECE/EEE) II-Year I-Semester Regular(CBCS) Examinations will be held in November/December, 2017 as per the following schedule.

Time: 2.00 pm to 5.00 pm

Date & Day	Paper	CSE	ECE	EEE
23.11.2017 Thursday	I	Mathematics-III	Electronic Devices	Mathematics-III
25.11.2017 Saturday	II	Mathematical Foundations of Computer Science	Switching Theory and Logic Design	Electrical Circuits-I
28.11.2017 Tuesday	III	Data Structures using C++	Signal Analysis & Transform Techniques	Electromagnetic Fields
30.11.2017 Thursday	IV	Computer Organization	Network Analysis and Synthesis	Digital Electronics & Logic Design
04.12.2017 Monday	V	Digital Logic Design	Pulse and Digital Circuits	Electronic Engineering-II
06.12.2017 Wednesday	VI	Environmental Studies	Mathematics-III	Environmental Studies
08.12.2017 Friday	VII	===	===	Managerial Economics & Accountancy

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OFFICE OF THE EXAMINATION BRANCH

MAHATMA GANDHI UNIVERSITY
NALGONDA - 508 254

No.156/Exams/MGU/2017-18

Date: 18.11.2017

TIME-TABLE

B.Tech. CSE /ECE/EEE) III-Year I-Semester Regular/Backlog Examinations will be held in November/December,2017 as per the following schedule.

Time: 2.00 to 5.00 pm

Date & Day	Paper	CSE	ECE	EEE
24.11.2017 Friday	I	Database Management Systems	Linear Integrated Circuits and Applications	Power Systems-II
27.11.2017 Monday	II	Operating Systems	Digital Integrated Circuits and Applications	Electrical Machinery-II
29.11.2017 Wednesday	III	Automata, Languages and Computation	Analog Communications	Power Electronics
02.12.2017 Saturday	IV	Software Engineering	Automatic Control Systems	Digital Electronics and Logic Design
05.12.2017 Tuesday	V	Managerial Economics and Accountancy	Computer organization and Architecture	Linear Integrated Circuits
07.12.2017 Thursday	VI	Design & Analysis of Algorithms	Electronic Instrumentation	Linear Control Systems

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TIME-TABLE

B.Tech. (CSE /ECE/EEE) IV-Year I-Semester Regular /Backlog Examinations will be held in November/December, 2017 as per the following schedule.

Time: 10.00 a.m to 1.00 pm

Date & Day	Paper	CSE	ECE	EEE
23.11.2017 Thursday	I	Distributed Systems	Microwave Engineering	Power System Operation and Control
25.11.2017 Saturday	II	Artificial Intelligence	VLSI Design	Electric Drives and Static Control
28.11.2017 Tuesday	III	Information Security	Computer Networks	Power Quality
30.11.2017 Thursday	IV	Principles & Applications of Embedded Systems	Mobile Cellular Communication	High Voltage Engineering
04.12.2017 Monday	V	Mobile Computing	Embedded Systems	---
06.12.2017 Wednesday	VI	Adhoc And Sensor Networks	Industrial Administration and Financial Management	---

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