



OFFICE OF THE CONTROLLER OF EXAMINATIONS  
MAHATMA GANDHI UNIVERSITY  
NALGONDA - 508 254  
TIME-TABLE

Time: 10.00 to 1.00 P.M

**B. Tech. (CSE /ECE/EEE) IV-Year II-Semester Regular/Backlog Examinations May-2019**

Date	Paper	CSE	ECE	EEE
04.05.2019	I	Data Mining	Reader Satellite Communication	Utilization
08.05.2019	II	Software Reuse Techniques	Wireless Sensor Networks/Coding Theory & Techniques	Industrial Administrations & Financial Management
11.05.2019	III	Information Retrieval Systems / Advanced Databases	Global Positioning System	<b>Elective:</b> Electro Power Distribution Engineering/HVE
15.05.2019	IV	----	----	Renewable Energy Sources

Note:

1. The candidates whose forms are rejected by the Examination Branch should not be examined and if any candidate is found not eligible at the later stage his/her registration will be cancelled for this exam. Any omission or clash may be intimated to the Controller of Examinations, MGU, immediately on the receipt of the Time Table.
2. Cell Phones and programmable calculators are strictly not allowed into the Examination Hall.
3. The responsibility of obtaining correct question paper from the Invigilator at the Examination Hall rests with the candidate. Answering a wrong Question paper may lead to Cancellation of Result.
4. The candidates have to carry the college identity card along with their Hall Ticket during the Examination.

Controller of Examinations



OFFICE OF THE CONTROLLER OF EXAMINATIONS  
MAHATMA GANDHI UNIVERSITY  
NALGONDA - 508 254  
TIME-TABLE

Time: 2.00 to 5.00 P.M

B. Tech. (CSE /ECE/EEE) Semester VI-Regular (CBCS) May, 2019

Date	Paper	CSE	ECE	EEE
04.05.2019	I	Web Technologies	Digital Communication	Electrical Machines-III
08.05.2019	II	Computer Networks	Digital Signal Processing	Microprocessors and Microcontrollers
11.05.2019	III	Object Oriented Analysis Design	Digital System Design Using Verilog HDL	Switchgear and Protection
15.05.2019	IV	Machine Learning	Antennas and Wave Propagation	Digital Signal Processing & Applications
17.05.2019	V	<b>Open Elective-I</b> Digital System Design Using Verilog HDL/Disaster Management/Entrepreneurship Development/Intellectual Property Rights	Managerial Economics and Accountancy	<b>Professional Elective-III</b> Power Quality Engineering/Programmable Logic Controllers/Digital Control Systems
20.05.2019	VI	<b>Professional Elective-II</b> Principles of Programming Languages/Software Architecture & Design Patterns/Microprocessors & Microcontrollers/Simulation & Modeling	<b>Professional Elective-I</b> Digital Image Processing/Computer Organization and Architecture/Optical Fiber Communications/Multi Rate Signal Processing	<b>Open Elective-I</b> Material Handling/Micro – Electro –Mechanical Systems/Disaster Management/Geospatial Techniques/Operating Systems/OOPs Using Java/Embedded Systems/Digital System Design Using Verilog HDL/Reliability Engineering/Industrial Robotics/Intellectual Property Rights/Intellectual Property Rights/Engineering Applications in Medicine

Controller of Examinations

Copy to:

1. The Principal UCET, MGU
2. Director , Academic Audit, MGU
3. P.S. to Vice-Chancellor, MGU
4. P.A. to Registrar